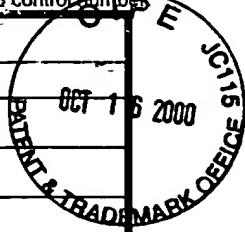


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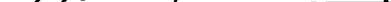
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	09/608,537
				Filing Date	6/30/2000
				First Named Inventor	Vishwanathan et al.
				Group Art Unit	<del>200</del> 3724
				Examiner Name	Eley
				Attorney Docket Number	126
(use as many sheets as necessary)				OCT 1 PATENT & TRADE	
Sheet	1	of	1		



#### **OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<i>TK</i>		MURARKA, S. P., STEIGERWALD, J., GUTMANN, R. J., "Inlaid Copper Multilevel Interconnections Using Planarization by Chemical-Mechanical Polishing", MRS Bulletin, June 1993, pp. 46-51	

Examiner Signature		Date Considered	04/07/03
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**Complete if Known**

Application Number	09/608,537
Filing Date	6/30/2000
First Named Inventor	Vishwanathan et al.
Group Art Unit	<del>3724</del> 3724
Examiner Name	Eley
Attorney Docket Number	126

U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

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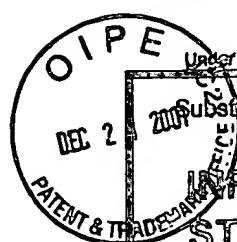
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Sheet 1 of 1

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<b>Application Number</b>	09/608,537
<b>Filing Date</b>	6/30/2000
<b>First Named Inventor</b>	Vishwanathan et al.
<b>Group Art Unit</b>	2812
<b>Examiner Name</b>	
<b>Attorney Docket Number</b>	126

U.S. PATENT DOCUMENTS

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Sheet

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<b>Application Number</b>	09/608,537
<b>Filing Date</b>	6/30/2000
<b>First Named Inventor</b>	Vishwanathan et al.
<b>Group Art Unit</b>	3724
<b>Examiner Name</b>	T. Eley
<b>Attorney Docket Number</b>	126

## **U.S. PATENT DOCUMENTS**

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